

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

Office of the Controller of Examinations

M.Tech III Semester (R20/R21) Regular Examinations, October 2024

Timings: 10:00 a.m. to 1:00 p.m.

TIME TABLE

7th October 2024

Date	Structural Engineering	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning) (R21 Regulation)
15.10.2024 (Tuesday)	Earthquake Resistant Design of Structures (CE3921)	Lower Power VLSI Design (EC4922)	Recommender Systems (CS4923)
17.10.2024 (Thursday)	Sustainable Development (CE3924)	Sustainable Development (CE3924)	Sustainable Development (CE3924)

Note: Seating arrangement will be notified on the day of each examination half an hour before the commencement of the examination.


Controller of Examinations


Principal

Copy to: HODs – CE/ECE/CSE (AI&ML) for Notice Board display/Exam Section
Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)